



Chipedge VLSI
Admission Test 2024
(CVAT 2024)

Launch Your VLSI Career with ChipEdge

Are you an Electronics Graduate looking to start your career in the Semiconductor Industry ?

ChipEdge brings you an exclusive opportunity to kickstart your career with our job-oriented VLSI certification courses. Our intensive classroom training along with placement assistance ensures that you are well-equipped to embark on a successful journey in the VLSI design domain.



ChipEdge

**11+ years of Excellence
in VLSI Education**

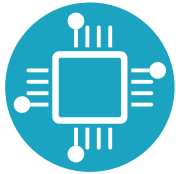
Key Features



Scholarship Opportunities for **5% Top Performers** per batch in the **CVAT 2024**



“Pay-After-Placement” **95%** of the seats per batch for the qualified in the **CVAT 2024**



Classroom Training with **40 seats only** per Batch



Exclusive Placement Opportunities with leading Semiconductor Companies



Courses designed as per Industry skill Requirements

List of Job Oriented VLSI Courses

Design Verification

- Batch 1: 10th June 2024
- Batch 2: 08th July 2024

Physical Design

- Batch 1: 10th June 2024
- Batch 2: 08th July 2024



Course Duration & Model

- 6 Months Intensive Offline Course in Bangalore
- 5-Day/week training in offline/classroom model
- Timing: 9:30 am to 4:30 pm, Theory class + Lab work

Admission Test Dates

- **Test 1:** 5th & 19th May 2024 (for the Batch Starting on 10th June 2024)
- **Test 2:** 2nd June 2024 (for the Batch Starting on 8th July 2024)

Interview dates will be communicated through Email/Mobile

Eligibility Criteria

Education Background

- B.Tech in ECE/EEE/IE/Telecom.
- M.Tech in VLSI/ Embedded / Power Electronics/ Digital communication, etc..
- For B.Tech & M.Tech Graduates, the passing year must be 2023 or 2024.



EDA Partner

SYNOPSYS®

Academic Performance

- Applicants must have a minimum aggregate score of 7.0 CGPA in their qualifying degree.

Registration Procedure

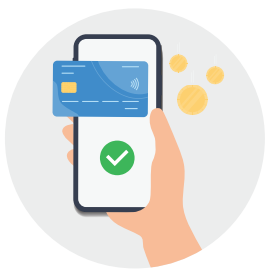
- Applicants must follow the specified test registration procedure outlined below.



Scan & Fill the Form



Registration Link



Complete the Payment Process ₹ 299



Prepare for the Test

Admission Test Syllabus for C-VAT 2024



Aptitude - Verbal aptitude, Quantitative aptitude, Analytical aptitude, Spatial aptitude

Digital Design- Digital Systems and Binary Numbers, Boolean Algebra and Logic Gates, Gate Level Minimization, Combinational Logic Design, Sequential Logic Design, Semiconductor Memories

Electronics Devices and Circuits: Semiconductors Physics (Energy bands in semiconductors, Equilibrium carrier concentration, Diffusion current & drift current, Mobility and resistivity), Diode (P-N junction, Zener diode, LED, photo diode & solar cell), Diode Circuits (Clipping, clamping & rectifiers), Transistors (BJT, MOSFET), Amplifiers, Op-amp Circuits

Network Analysis: Circuit Components, DC Circuits, Kirchhoff's Laws, Node and Mesh analysis, Source Transformation, Network Theorems (Superposition, Thevenin's theorem, Norton's theorem), Series Resonance Parallel Resonance

Program Fees

Course fee: 1,50,000 + 18% GST

Scholarship up to 80% of fees

Pay after Placement:

Pay 60% during the training and balance 40% after Placement.

- Rs. 90,000 + 18% GST (During the training)
- Rs. 60,000 + 18% GST (After Placement)



200+ Hiring Semiconductor Companies



For more details, Call: 8448448869, Whatsapp: 9611153120
 Visit our website: www.chipedge.com